



**Silicon Motion, Inc.**

## **SM3255EN Test Program and ISP Release Note**

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Release Date	ISP Version	ISP Check Sum	Test AP Version	Description
2010/09/30	101027-AA-	SM3255ENAAISP.BIN 0x 005E52A9	V2.03.34 v1 10/11/04	<ol style="list-style-type: none"> <li>1. SM3255ENAAISP-B74 support Intel 29F64G08AATE1 (B74A) and set ECC to 64bit ECC.</li> <li>2. SM3255ENAAISP support SEC 27nm TLC K9ACGD8U0M toggle mode flash.</li> <li>3. SM3255ENAAISP support SEC 27nm TLC K9ABGD8U0B toggle mode flash.</li> <li>4. Set the /WP high in suspend mode to fix the suspend current issue.</li> <li>5. SM32Xtest.exe can support SM3255ENA1 IC version auto-load.</li> </ol>
2010/11/15	101111-AA-	SM3255ENAAISP.BIN 0x 005E5500	V2.03.34 v1 10/11/04	<ol style="list-style-type: none"> <li>1. Fix power-cycling FAT table block push to spare 0xBFFF..</li> </ol>
2010/11/22	101118-AA-	SM3255ENAAISP-B74.BIN 0x 000532F2 SM3255ENAAISP.BIN 0x 005E5382	V2.03.34 v12 10/11/22 Build	<ol style="list-style-type: none"> <li>1. Fix 5V normal power cycling fail issue.</li> <li>2. SM3255ENAAISP-B74-M.BIN to support Micron B74 flash 2plane mode only.</li> <li>3. Support SEC 27nm TLC 32Gb K9ABGD8U0B (EC D7 98 CE)</li> <li>4. Fix suddenly power-cycling FAT-Table block find last page issue (1k2)</li> </ol>
2010/12/20	101213-AA-	SM3255ENA1ISP-B74.BIN 0x 005EFAA7 SM3255ENAAISP.BIN 0x 005CB008	V2.03.35 v7 10/12/14 Build	<ol style="list-style-type: none"> <li>1. Fix 384page to keep the new bad block with Erase Good Block only function.</li> <li>2. Fix Micron Flash B74A ATTO drop fail issue.</li> <li>3. Fix IM Flash HDbench drop.</li> </ol>



				<ol style="list-style-type: none"> <li>Fix 5V normal power-cycling issue.</li> <li>Fixed the stand-by/un-config current is high issue.</li> <li>Set the LDO setting as V1.8=0x07,V3.3=0x05 in CID: 0x16Eh</li> <li>MP set LoopTestCount=0 in default.ini</li> <li>MP fixed SM3255ENAA show bad block issue with ISP exist.</li> <li>MP fixed StrongPage Table for SM3255ENA2 with Intel B74A</li> </ol>
2011/01/05	101220-A1-	SM3255ENA1PTEST.BIN 0x 00213DD1	V2.03.35 v11 10/12/30 Build	<ol style="list-style-type: none"> <li>Pretest fix the pair page table for 384page flash.</li> <li>MP Add QC debug option.</li> </ol>
2011/01/28	110127-A1	SM3255ENA1ISP.BIN 0x 005CEF4E SM3255ENA1ISP-B74A.BIN 0x 005F90E4	V2.03.36 v9 11/01/27 Build	<ol style="list-style-type: none"> <li>Fixed 5V normal Power cycling with WL issue</li> <li>Reduced the regular device configuration is under 100ms.</li> <li>Fixed SONY PS3 compatibility issue.</li> <li>Modified the SEC 27nm TLC read-retry table.</li> <li>Fixed IM B74A USB disconnection issue.</li> <li>MP fixed initial fail with 192page dual die flash.</li> <li>MP added external execute file call function with tool initial and destroy.</li> <li>MP external option to bypass check USB 2.0.</li> <li>MP fixed to support two current limit control board.</li> <li>MP fixed Debug function with read memory address.</li> <li>MP fixed software serial number function with SM3255AB &amp; SM3257 series.</li> <li>MP fixed open short test flow.</li> </ol>
2011/01/31	110127-AA	SM3255ENA1ISP-B74A.BIN	V2.03.36 v9	<ol style="list-style-type: none"> <li>SM3255ENA1ISP-B74A to support LED function.</li> </ol>



		0x 005F94FB	11/01/27 Build	
2011/03/10	110225-A1 110310-A1	SM3255ENA1ISP.BIN 0x 005D3F8F SM3255ENA1ISP-B74.BIN 0x 01BE41D1	V2.03.37 v9 11/03/10 Build	<ol style="list-style-type: none"><li>1. SM3255ENA1ISP-B74A fixed the performance drop issue on SDP.</li><li>2. SM3255ENA1ISP-B74A fixed WinCE 6.0 compatibility issue.</li><li>3. SM3255ENA1ISP fixed 5V normal power cycling issue when change child to FAT, last page in temp block of CE1-block can't be found</li><li>4. SM3255ENA1ISP fixed the un-recognized device and format fail after SPOR test.</li><li>5. SM3255ENA1ISP fixed compare fail and read actual data 0x00 after SPOR test.</li><li>6. MP added Ethernet communication with Hontect Auto-Handler HT-3315.</li><li>7. MP enable function - B74 flash download ISP with weak page write random data.</li><li>8. MP added ADATA function for auto-Handler HT3315 connect and agent used.</li><li>9. MP added auto-handler 3315 connect reference file for ADATA.</li><li>10. MP fixed MP tool FAT mode preload file crash with long filename or directory name.</li></ol>